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From: Robert D. Marshall, Jr.  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-917-5290

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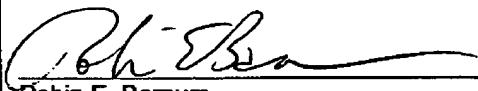
DEC 09 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of **TI-30481**  
Gary L. Swoboda, et al. **Art Unit: 2128**  
Serial No.: 09/943,599 **Examiner: Akash Saxena**  
Filed: August 30, 2001 **Conf. No.: 2479**  
For: Correlating On-Chip Data Processor Trace Information for Export

**CERTIFICATION OF FACSIMILE TRANSMISSION**

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<input type="checkbox"/> <b>DIVISIONAL APPN</b>	
<b>NAME OF INVENTOR(S):</b> Gary L. Swoboda, et al.	
<b>TITLE OF INVENTION:</b> Correlating On-Chip Data Processor Trace Information for Export	
<b>TI FILE NO.:</b> <b>TI-30481</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 12/9/05	
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Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

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RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda et al Art Unit: 2128  
Serial No.: 09/943,599 Examiner: Akash Saxena  
Filed: August 30, 2001 Docket: TI-30481  
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

**AMENDMENT UNDER 37 C.F.R. 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATION OF FAX TRANSMITTAL  
UNDER 37 C.F.R. §1.6(b)**

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated September 9, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: